

DATA SHEET

LCD MODULE

MODULE NO.:

TCB1602AA SERIES

Customer:		
Approved by:		

Sonytek Display Co., Ltd.						
Approved by	Checked by	Prepared by				

RECORDS OF REVISION

TCB1602AA1.0First issueSept. 6th, 2012TCB1602AA2.0Update Mounting holesAug. 16th, 2017TCB1602AA3.0Change BacklightOct. 26th, 2017	Part Number	Revision	Revision Content	Revised on
	TCB1602AA	1.0	First issue	Sept. 6th, 2012
TCB1602AA 3.0 Change Backlight Oct. 26th, 2017	TCB1602AA	2.0	Update Mounting holes	Aug. 16th, 2017
	TCB1602AA	3.0	Change Backlight	Oct. 26th, 2017
	TCB1002AA	5.0	Change Backright	Oct. 20th, 2017

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1. FUNCTIONS & FEATURES

• TCB1602AA Series LCD type:

Module	LCD Type	Remark
TCB1602AA-SBW	STN-Blue Transimmive Negative Mode	VDD=3.0V
TCB1602AA-SYG	STN Yellow-Green Transimmive Positive Mode	VDD=3.0V
TCB1602AA-SBWB	STN-Blue Transimmive Negative Mode	VDD=5.0V
TCB1602AA-SYGB	STN Yellow-Green Transimmive Positive Mode	VDD=5.0V
TCB1602AA-SGW	STN-Gary Transimmive Positive Mode	VDD=5.0V

• Display Contents :16 * 2 Characters (5*8 dots)

• Driving Scheme : 1/16Duty; 1/5Bias

Viewing Direction :6 0' clock
 Driver IC :AIP31066
 Interface :Parallel

Backlight :white/Yellow-Green
 Operating Temperature :-20 C-+70 C
 Storage Temperature :-30 C-+80 C

RoHS Compliant

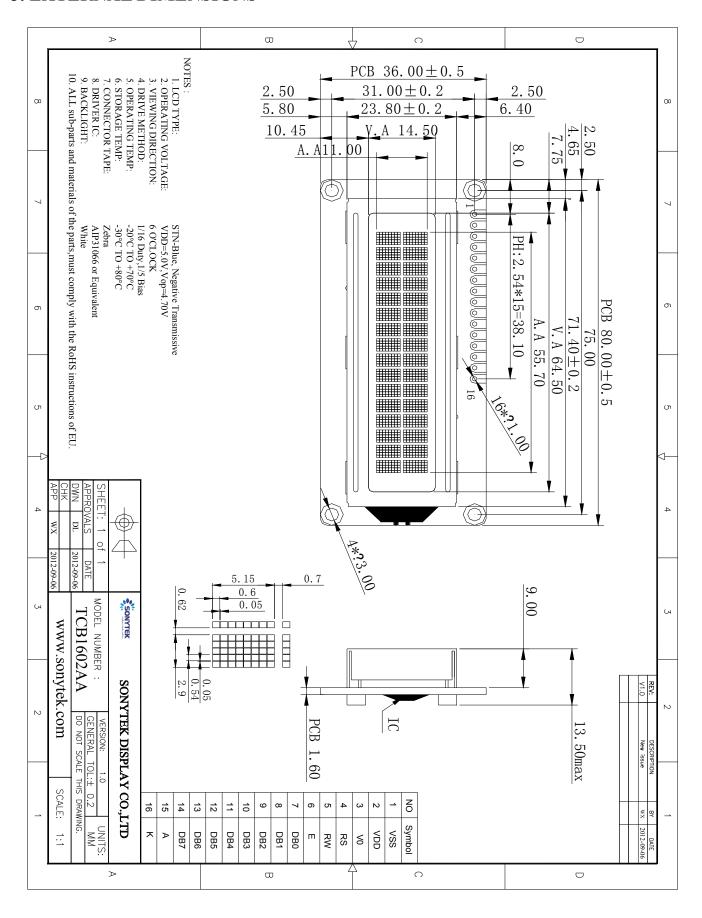
2. MECHANICAL SPECIFICATIONS

• Outline Dimensions : 80.00(W) x 36.00(L) x 13.50max(H)(mm)

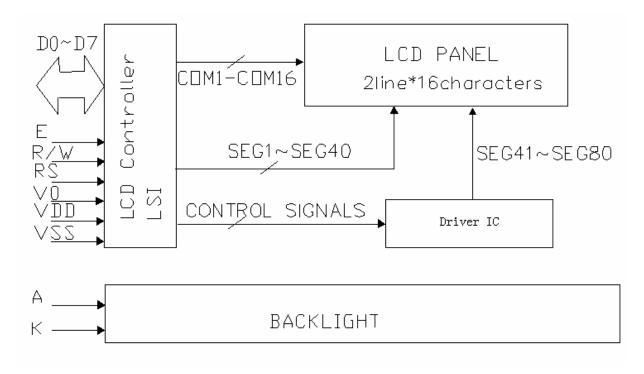
Viewing Area
 Active Area
 Character size
 Dot Pitch
 Dot Size
 (64.50 (W) x 14.50(L)(mm)
 (55.70 (W) x 11.00 (L)(mm)
 (2.90 (W) x 5.15 (L)(mm)
 (3.52 (W) x 5.85 (L)(mm)
 (0.59 (W) x 0.65 (L)(mm)
 (0.54 (W) x 0.60 (L)(mm)

• Weight : TBD

3. EXTERNAL DIMENSIONS



4. BLOC'K DIAGRAM



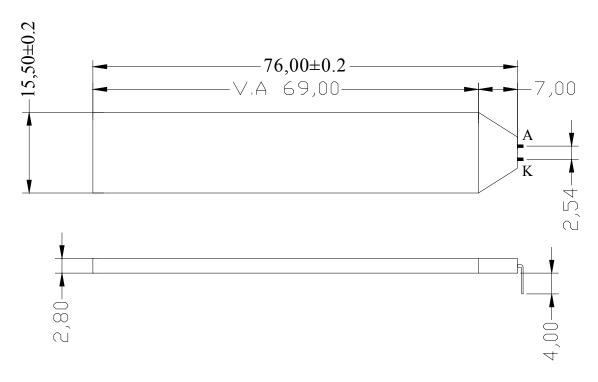
5. PIN ASSIGNMENT

Pin No.	Symbol	Function
1	VSS	Ground terminal of module.
2	VDD	Power terminal of module
3	V0	Power Supply for liquid crystal drive.
		Register select
4	RS	RS = 0···Instruction register
		$RS = 1 \cdots Data register$
		Read /Write
5	R/W	$R/W = 1 \cdots Read$
		$R/W = 0 \cdots Write$
6	Е	Read/Write Enable Signal
7	DB0	
8	DB1	
9	DB2	Bi-directional data bus, data transfer is performed once, thru DB0 to DB7,
10	DB3	in the case of interface data. Length is 8-bits; and twice, thru DB4 to DB7 in
11	DB4	the case of interface data length is 4-bits. Upper four bits first then lower
12	DB5	four bits.
13	DB6	
14	DB7	
15	A	Anode of Backlight
16	K	Cathode of Backlight

6. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS

Electrical/Optical Specifications

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	Vf	2.9	3.1	3.3	V	If= 15 mA
Reverse Current	Ir			100	μΑ	Vr=5.0 V
Dominant wave length	λD	569	572	575	nm	If= 15 mA
Spectral Line Half width	Δλ		25		nm	If= 15 mA
Luminous	Lv	75	80		cd/m ²	If= 15 mA



Remarks:

- 1: Unmarked tolerance is ± 0.2
- 2: Clolr: Yollew-Green
- 3: All materials comly with RoHS

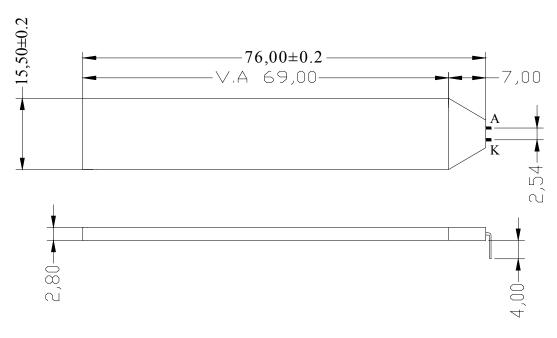


BACKLIGHT CIRCUIT

TCB1602AA-SYGB

Electrical/Optical Specifications

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	Vf	2.9	3.1	3.3	V	If= 15 mA
Reverse Current	Ir			100	μΑ	Vr=5.0 V
Dominant wave length	X					If= 15 mA
	Y				-	11– 13 IIIA
Spectral Line Half width	Δλ				nm	If= 15 mA
Luminous	Lv	70	80		cd/m ²	If= 15 mA



Remarks:

1: Unmarked tolerance is ± 0.2

2: Clolr: White

3: All materials comly with RoHS



BACKLIGHT CIRCUIT

TCB1602AA-SBWB/TCB1602AA-SGW

8. MAXIMUM ABSOLUTE POWER RATINGS

Item	Symbol	Standard value	Unit
Power supply voltage(1)	V_{DD}	-0.3~+7.0	V
Power supply voltage(2)	V_{LCD}	V _{DD} -10.0~V _{DD} +0.3	V
Input voltage	V _{IN}	-0.3~V _{DD} +0.3	V
Operating temperature	Topr	-20~+70	$^{\circ}$
Storage temperature	Tstg	-30~+80	$^{\circ}$

^{*}Voltage greater than above may damage to the Circuit.

9. ELECTRICAL CHARACTERISTICS

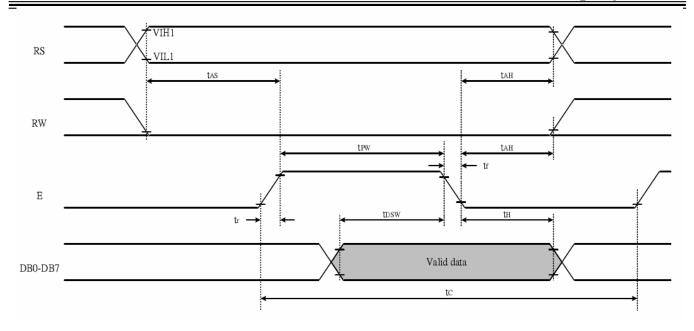
9-1 DC Characteristics

Item	Cymbol	St	andard Va	lue	Test	Unit
Item	Symbol	MIN	TYP	MAX	Condition	Omi
Operating Voltage	$V_{ m DD}$	4.8	5.0	5.2		V
Operating Voltage	$V_{ m DD}$	2.8	3.0	3.2		·
	I_{DD1}		TBD	1.0	Ceramic oscillation fosc=250kHz	
Supply Current	I_{DD2}		TBD	0.6	Resistor oscillation external clock operation fosc=270kHz	mA
LCD Driving Voltage	VLCD	4.5	4.7	4.9	V _{DD} -V ₀	V

9-2 AC Characteristics

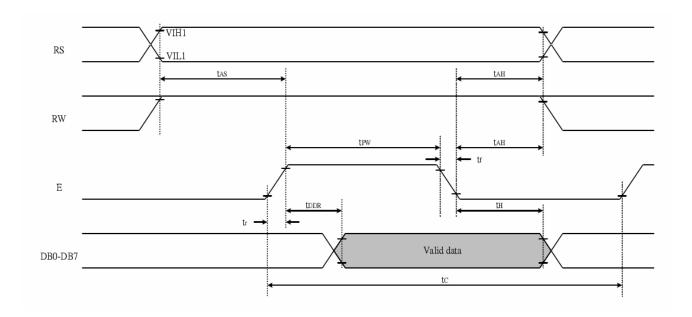
9.2.1 Write mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	$t_{\rm C}$	1200			ns	Е
Enable Pulse Time	T_{PW}	460			ns	Е
Enable Rise/Fall Time	T_R , T_F			25	ns	Е
Address Set-up Time	T_{AS}	0			ns	R/W,RS,E
Address Hold Time	T_{AH}	10			ns	R/W,RS,E
Data Set-up Time	T_{DSW}	80			ns	DB0~DB7
Data Hold Time	T_{H}	10			ns	DB0~DB7



9.2.2 Read mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	$t_{\rm C}$	1200			ns	E
Enable Pulse Time	T_{PW}	480			ns	E
Enable Rise/Fall Time	T_R , T_F			25	ns	E
Address Set-up Time	T_{AS}	0			ns	R/W,RS,E
Address Hold Time	T_{AH}	10			ns	R/W,RS,E
Data Set-up Time	T_{DDR}			320	ns	DB0~DB7
Data Hold Time	T_{H}	10			ns	DB0~DB7



10. DISPLAY DATA RAM (DDRAM)

Display Position	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM	00	01	02	03	04	05	06	07	08	09	0A	oΒ	0C	0D	0E	0F
Address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F
,																
For Shift	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10
Left	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50
For Shift	27	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E
Right	67	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E

11. INSTRUCTION TABLE

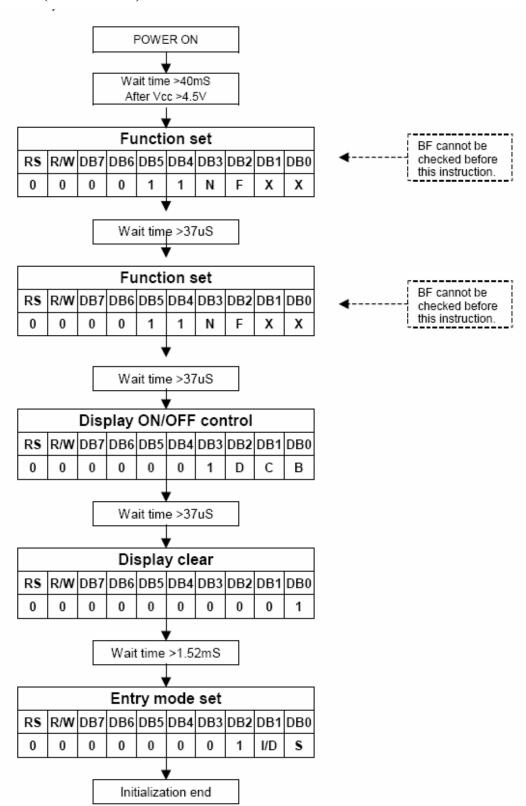
Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz)	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write"20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	Х	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	С	В	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	X	X	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	N	F	X	X	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	0us	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

Note:

Be sure the AIP31066 is not is not in the busy state (BF=00 before sending an instruction from the MPU to the AIP31066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

12. INITIALIZING BY INSTRUCTION

8-bit interface mode (fosc=270kHz)



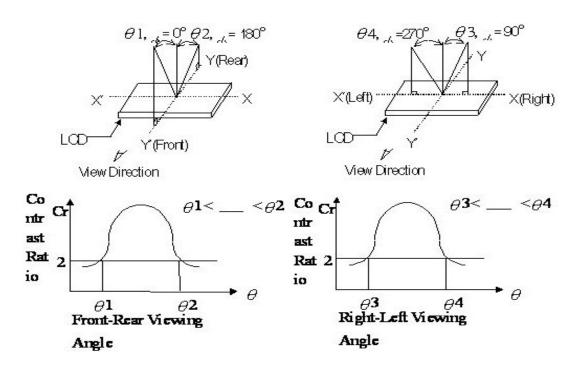
13. CHARACTER GENERATOR ROM

0A

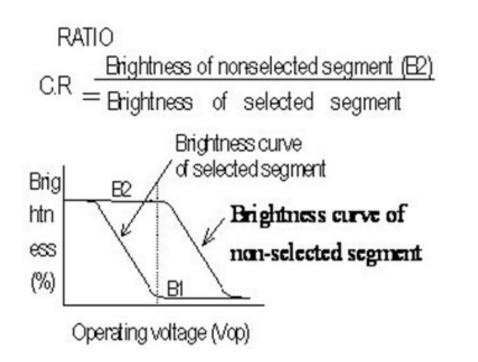
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)														8	ü
0110	(7)		88													
0111	(8)												×			
1000	(1)					×										**
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)											×				
1110	(7)															
1111	(8)														ä	

14. Optical Characteristics

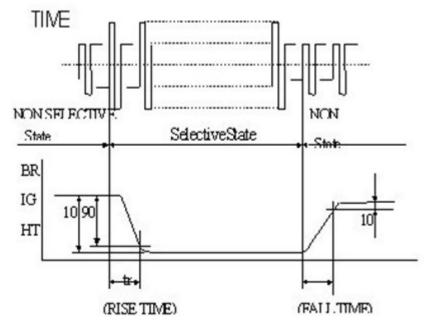
14.1 Definition of Viewing Angle



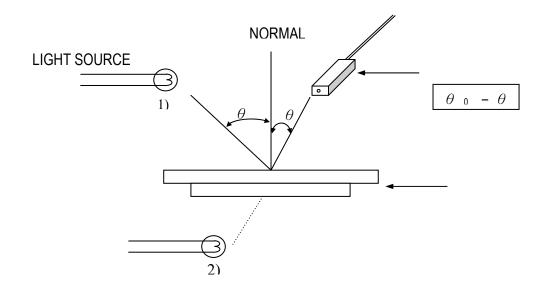
14.2 Definition of Contrast



14.3 Definition of Response



14.4 Measuring Instruments For Electro-optical Characteristics



* Note:

- 1) Light source position for measuring the reflective type of LCD panel;
- 2) Light source position for measuring the transflective / transmissive types of LCD panel.

15. RELIABILITY TEST

Operating life time: Longer than 50,000 hours

(at room temperature without direct irradiation of sunlight)

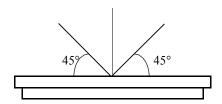
Reliability characteristics shall meet following requirements.

No.	Test Item	Content of Test	Test Condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	+80°C 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	−30°C 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	+70°C 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	−20°C 96H
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and humidity storage for a long time	40°С 90%RH 96Н
6	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}\text{C} \longrightarrow 25^{\circ}\text{C} \longrightarrow 70^{\circ}\text{C} \longrightarrow 25^{\circ}\text{C}$ 30min 5min 30min 5min \longrightarrow 1 cycle	-20°C/70°C 5 cycles
7	Vibration Test (Package State)	Endurance test applying the vibration during transportation	10Hz-55Hz, 50m/s,15min
8	Shock Test (Package State)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s, 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40 kPa 16 H

16. Inspection specification

16.1 Visual Inspection

- 1) Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- 2) Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- 3) Inspect the module at 45° right and left, top and bottom.
- 4) Use the optimum viewing angle during the contrast inspection.



16.2 Standard of Appearance Inspection

No.	Item	Criteria									
		F	Round type: as p	per following drawin	following drawing						
		•	$\Phi = (X+Y)/2$								
				Size	cceptable quantity Zone A	Zone B					
		6		Ф<0.1	Any number						
			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ 	0.1<Ф<0.2	2	Any number					
			X	0.2<Ф<0.25	1	Any number					
				0.25<Ф	0						
		I	Line type: as pe	r following drawing Accepta	ble quantity						
	Black spot		Length	Width	Zone A	Zone B					
1	White spot		_	W≤0.02	Any num ber	Any					
	Dust		L≤3.0	0.02 <w≤0.03< td=""><td>2</td><td>nu</td></w≤0.03<>	2	nu					
			L≤2.5	0.03 <w≤0.05< td=""><td>2</td><td>mb</td></w≤0.05<>	2	mb					
			_	0.05 <w< td=""><td>As round type</td><td>er</td></w<>	As round type	er					
		/ -	W L Total acceptable	e quantity: 3	, , , , , ,						
2	Polariser	5	Scratch on prote	ective film is permitte	ed						

	scratch	Scratch on polariser: sa	me as No. 1			
		$\Phi = (X+Y)/2$				
				Acceptabl	le quant	itv
		Ψ Y	Size		ne A	Zone
		X	Size	+		Zonc
				A	ny	
				_	nu	
	D 1 .		Ф<0.	2	m	
3	Polariser	Total acceptable			be	
	bubble	quantity: 3			r	Any
		quantity. 5	0.2< ₵			r
			<().	2	n
			5			•
			0.5< ₵)		
			<1	1.	1	
			0)		
			1.0< ₵) (0	
		4.1 Pin hole on segmen	ted display			
		W: segment width	arspray			
		$\Phi = (A+B)/2$				
		₩ (A+D)/2				
		B		Acceptable o	quantity	
		B	Width		Quan	ntity
				Ф	≤0.2	and Φ
		\mathbf{A}_{\nearrow} \mathbf{A}	W≤0.4	-		≤1/2W
		\rightarrow \swarrow		Φ≤0		
		\mathbf{W}^{\prime}	W > 0.4	Φ≤1		ıu
			Total accepta			et ner
		4.6.77	segmen	t	. i deie	or per
		4.2 Pin hole on	Pin holes	with P unc	der 0.1	0 mm a
		dot matrix	acceptal	ble		
		display				
			Г			
_	Segment	₩ ≤0.0	15.			quantity
4	deform	7, [٦ <u>ـٰـ</u> ,	Siz	e	
	ation	(d)ਕ			Any
		1				1
				a, b<	0.1	
						1
		Total acceptable quanti	ty: 7			
				·		Any
		4.3 Segments / dots wit	th different			1
		width		(a+b)/2	2≤0.1	
		Γ.		` '		1
		D O				
			-	0.5<	Φ	
					<1.0	3
		A B			eptable	
				Acce	piault	1
		- + -	-		_	><1/2
		- 11 1		a≥b a <b< td=""><td>a/t</td><td>o≤4/3 o>4/3</td></b<>	a/t	o≤4/3 o>4/3

1		Ф-(А	-R)/2				Specifica					
		$\Phi = (A - A)$	F D]/2									
		By By	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\									
						Acceptable quantity						
				,	Size		A nv.					
		m		^ </td <td></td> <td></td> <td>Any nu</td>			Any nu					
		'		27	Φ≤0.4		m					
		\geqslant		27	_~		be					
							r					
			- -C -		0.4<Ф							
					≤1.		5					
		Total acc	ceptable quant	ity: 7	0							
					1.0<Ф							
					≤1.		3					
					5 15~D							
					1.5<Ф ≤2.		2					
					<u></u>		2					
	Colour					<u> </u>						
5	unifor	Level of sample for approval set as limit sample										
	mity											
	D1.111.4	The backlight colour should correspond to the product specification Flashing and or unlit backlight is not allowed										
6	Backlight		Dust larger than 0.25 mm is not allowed									
		Exposed wire bond pad is not allowed										
7	СОВ	Insufficient covering with resin is not allowed (wire bond line										
			_	bubble on the								
		No unmelted solder paste should be present on PCB										
	РСВ	Cold solder joints, missing solder connections, or oxidation are no allowed										
8				-11 DCD	B are allowed							
				onents are not								
		Short Ch	cuits on comp									
				Acceptable Size		mantity						
			On	Size		uantity Any						
			t			nu						
			r	Ф<0.2	2	mb						
			a			er						
	Tray		у	Ф>0.2	5	4						
9	particle		On	Ф ≥0	25	2						
	S		d									
			i									
			S	1 _ 2		1						
			р 1	L=3		1						
			a									
			у									

17. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling the LCD module.
 - -Tools required for assembly, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0

°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

18. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections